

Fig. 1

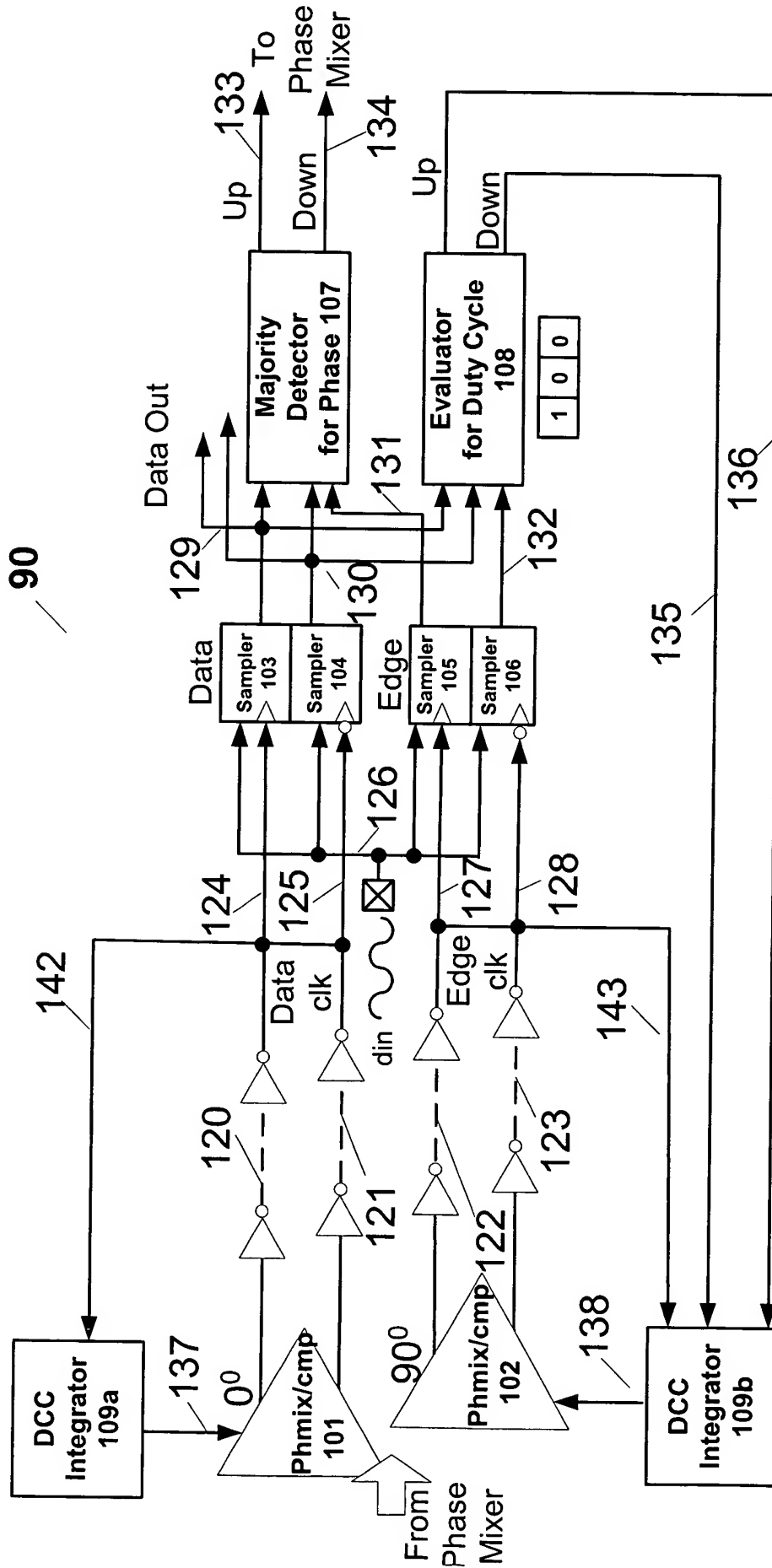


Fig. 2

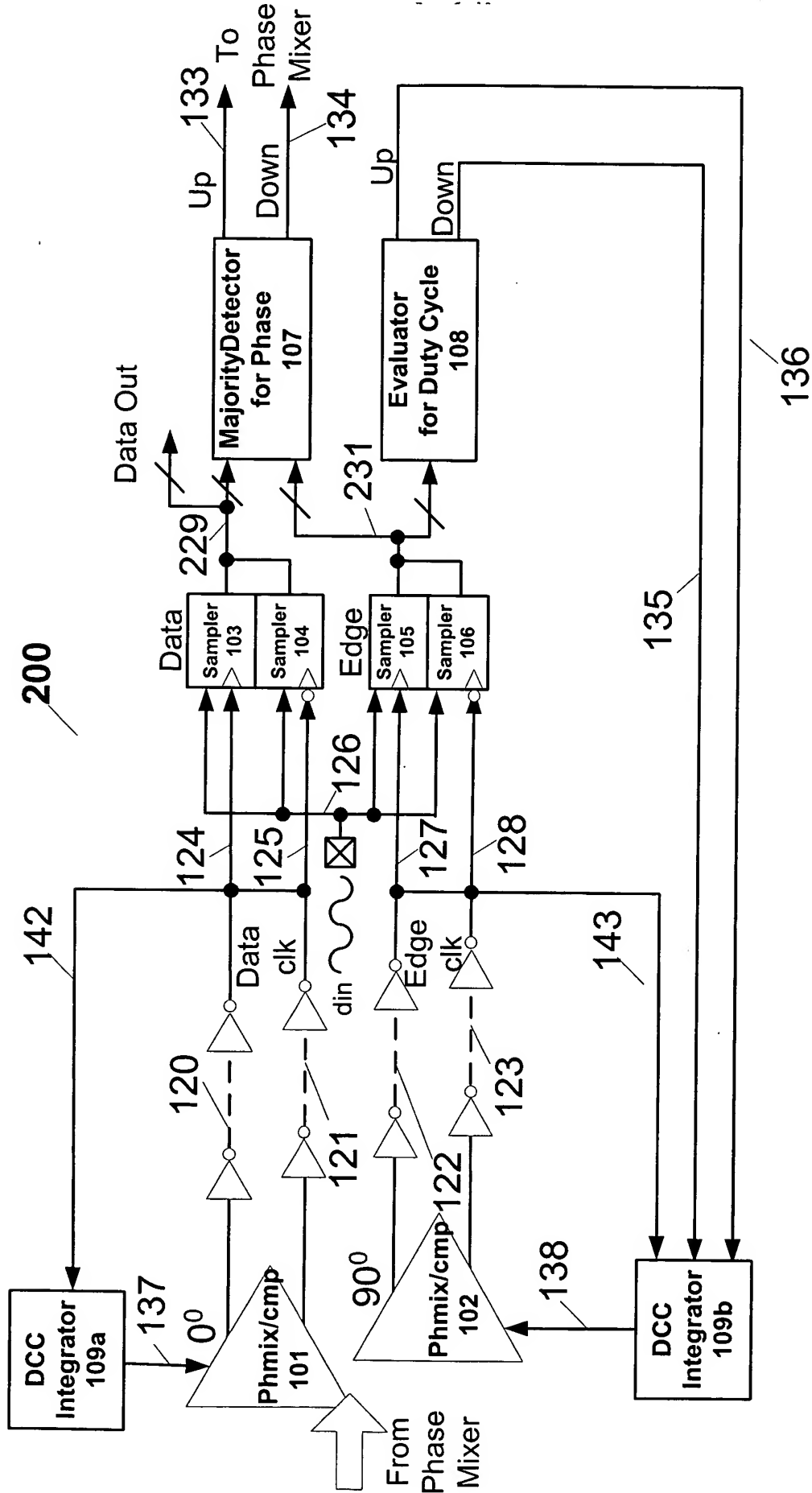
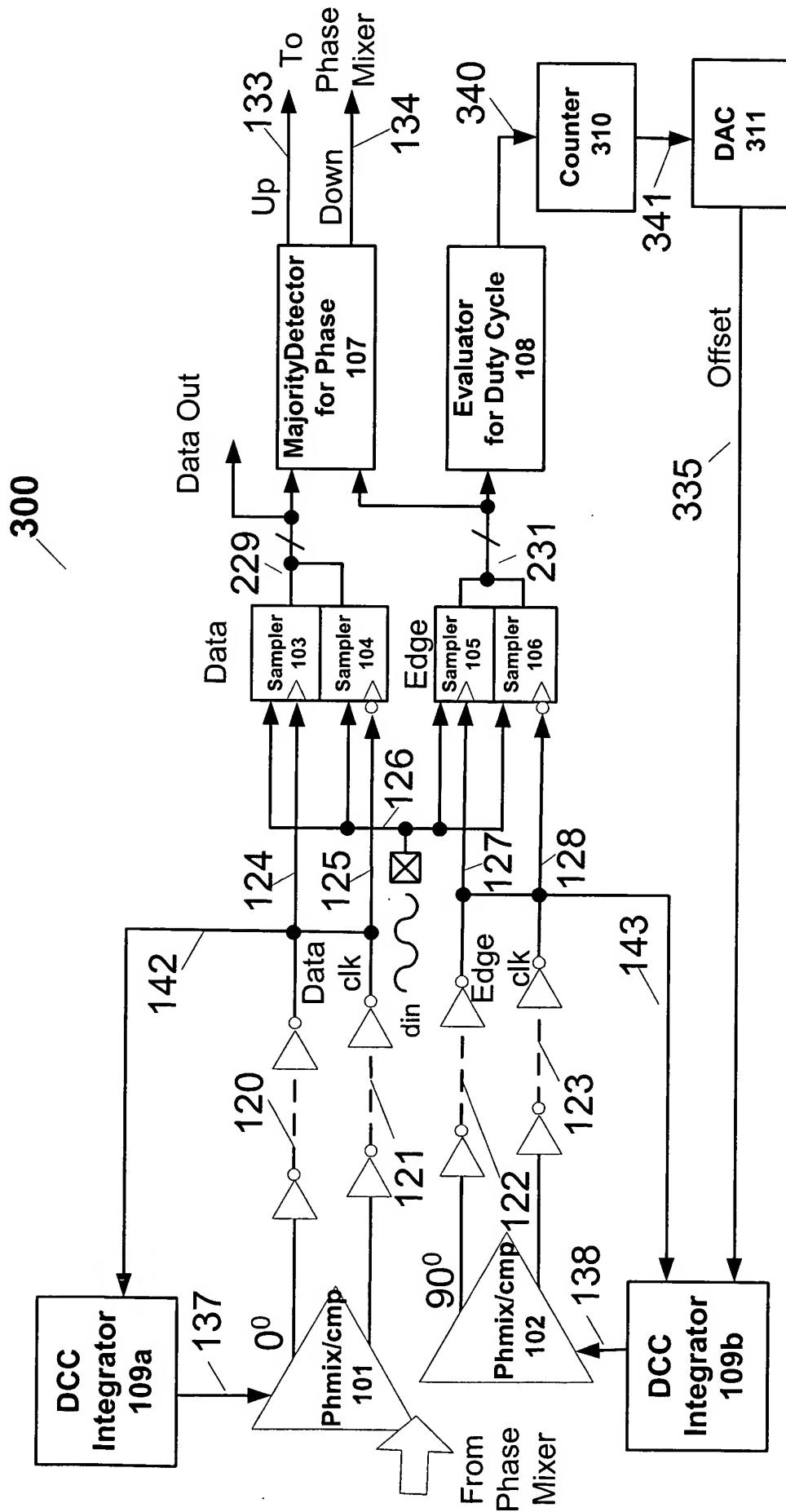


Fig. 3



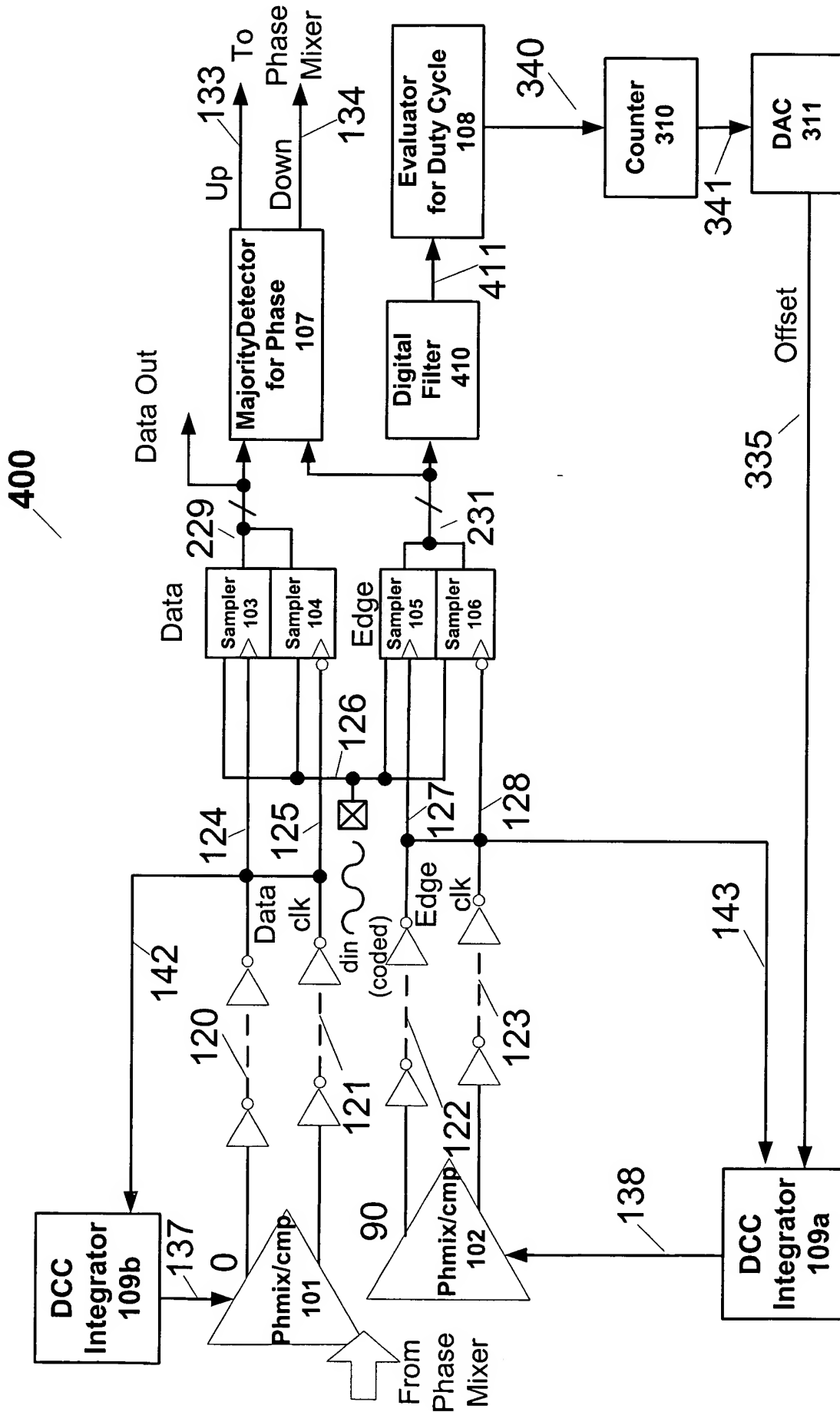


Fig. 5

Title: A Circuit, Apparatus And Method For Adjusting A Duty-Cycle Of A Clock
Signal In Response To Incoming Serial Data
Applicants: Jared Zerbe et al.
Appl. No.: Unknown
Filing Date: Herewith
Express Mail No.: EV 305480474 US

Docket: RAMB-01007US1
Atty: Kirk J. DeNiro
Phone: (415) 369-9660

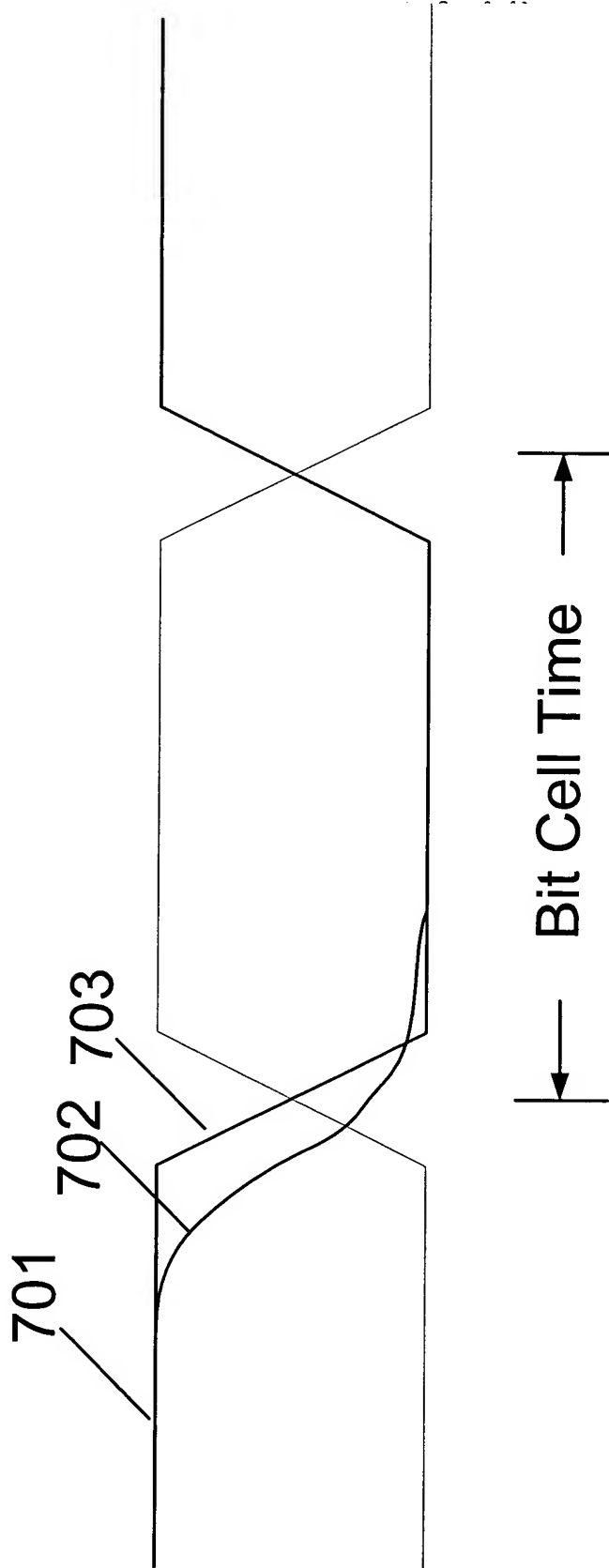


Fig. 6

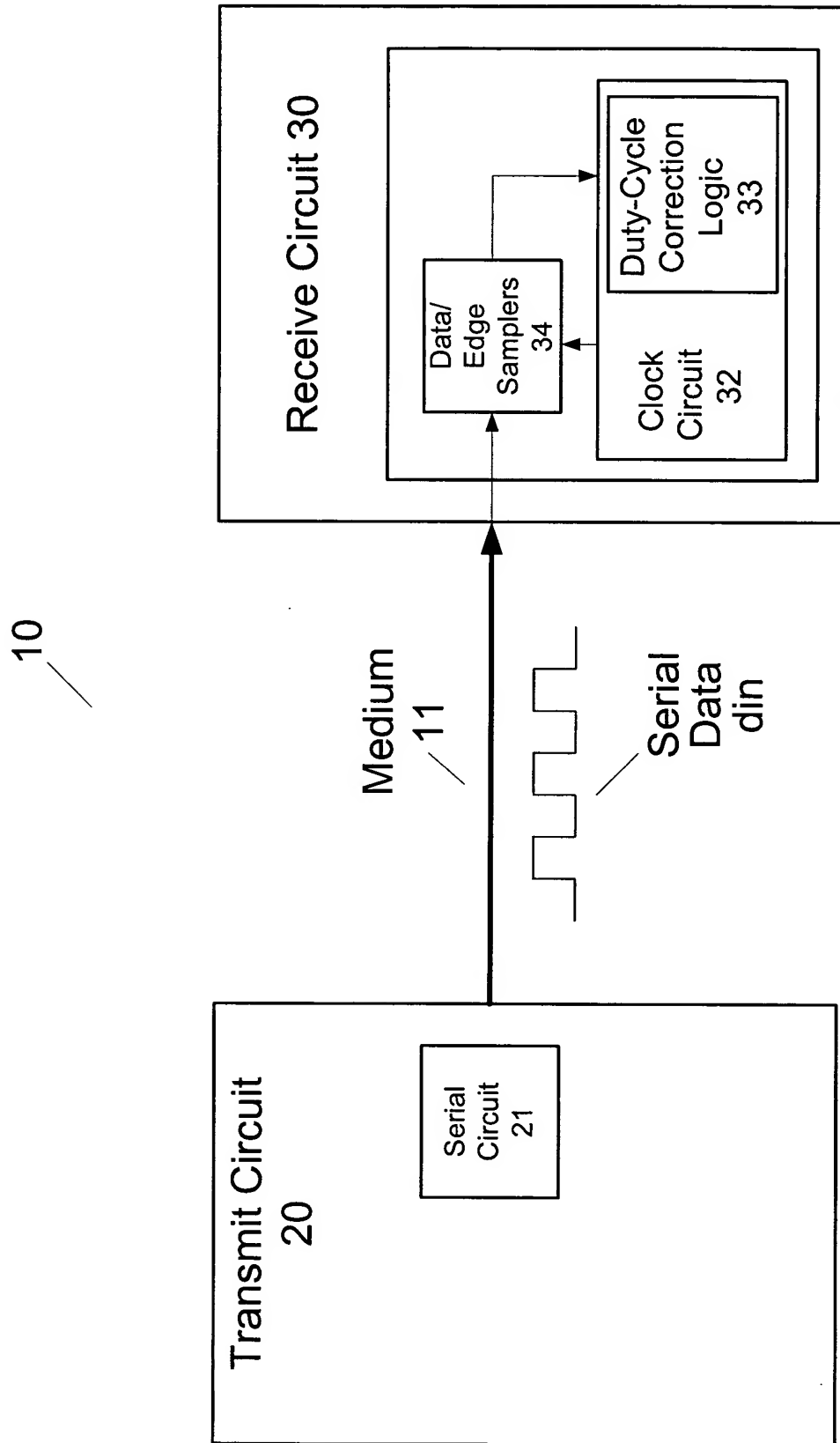


Fig. 7

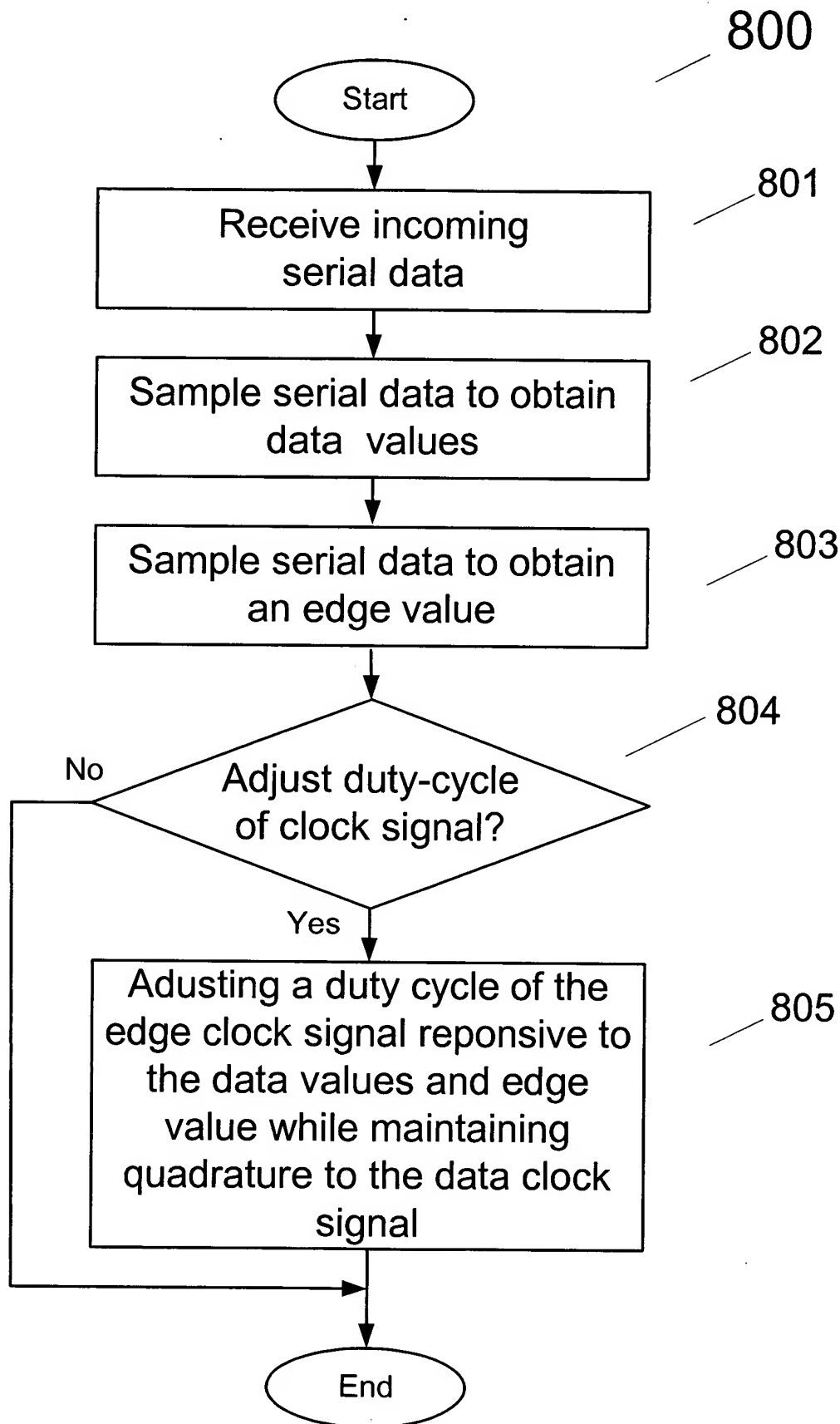


Fig. 8

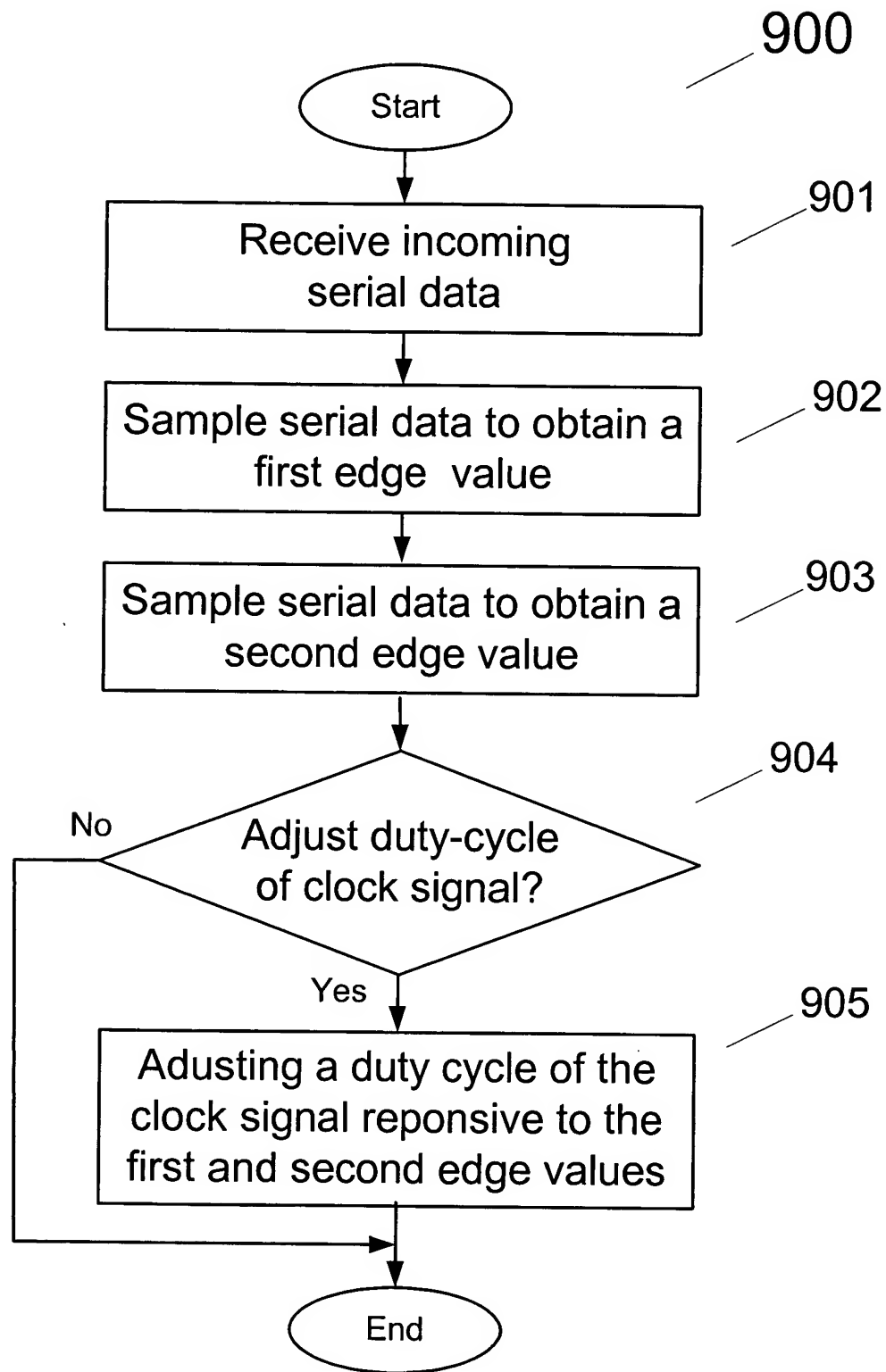


Fig. 9